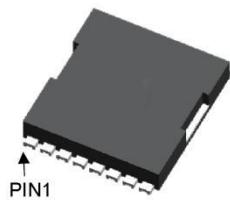


## SGT N-channel Power MOSFET

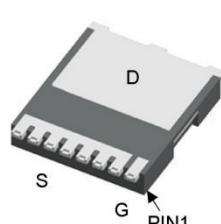
**MTR3R8N08TL**

**TOLL**

TOLL Top View



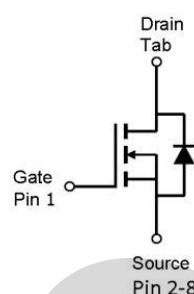
TOLL Bottom View



$V_{DS}$	80	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	2.7	$\text{m}\Omega$
$I_D$	160	A

### Features

- 1、Low on – resistance
- 2、Package TOLL
- 3、SGT N-channel Power MOSFET



### Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

**Maximum ratings, at  $T_A = 25^\circ\text{C}$ , unless otherwise specified**

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	80	V
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$ (Silicon limit)	A
		$T_C=25^\circ\text{C}$ (Package limit)	A
		$T_C=100^\circ\text{C}$ (Silicon limit)	A
$I_{DM}$	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	A
$E_{AS}$	Avalanche energy, single pulsed ②	1122	mJ
$P_D$	Maximum power dissipation	$T_C=25^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to +150	°C

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	0.6	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	53	°C/W

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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### Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
R <sub>D(on)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =50A	--	2.7	3.8	mΩ
G <sub>f</sub>	Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =50A	170	--	--	S

### Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V ,V <sub>DS</sub> =40V, f=1MHz	--	6575	--	pF
C <sub>oss</sub>	Output Capacitance		--	1020	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	539	--	pF
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	--	1.7	--	Ω
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>GS</sub> =10V,V <sub>DS</sub> =50V, I <sub>D</sub> =50A , ID=20A	--	74	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	23	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	20	--	nC

## Switching Characteristics

Td(on)	Turn-on Delay Time	$V_{GS}=10V$ , $V_{DS}=50V$ , $R_L=3.0\Omega$ , $T_j=25^\circ C$	--	32	--	ns
Tr	Turn-on Rise Time		--	53	--	ns
Td(off)	Turn-Off Delay Time		--	59	--	ns
Tf	Turn-Off Fall Time		--	34	--	ns

## Source- Drain Diode Characteristics@ $T_j = 25^\circ C$ (unless otherwise stated)

VSD	Forward on voltage	$I_{SD}=50A, V_{GS}=0V$	--	0.8	1.2	V
Trr	Reverse Recovery Time	$I_F=30A$ , $dI/dt=500A/\mu s$	--	45	--	ns
Qrr	Reverse Recovery Charge	$I_F=30A$ , $dI/dt=500A/\mu s$	--	80	--	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

- ② Limited by  $T_{Jmax}$ , starting  $T_J = 25^\circ C$ ,  $L = 0.5mH$ ,  $R_G = 25\Omega$ . Part not recommended for use above this value
- ③ The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^\circ C$ .
- ④ Pulse width  $\leq 380\mu s$ ; duty cycle  $\leq 2\%$ .

## Typical Performance Characteristics

Fig 1: Output Characteristics

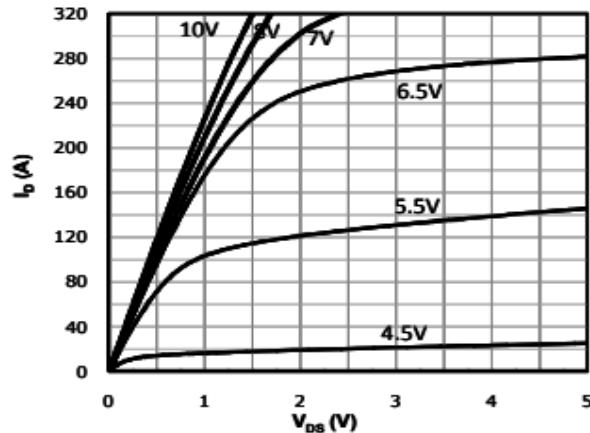


Fig 2: Transfer Characteristics

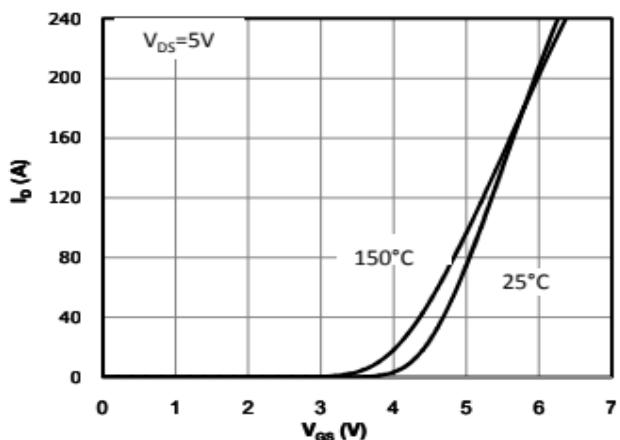


Fig 3:  $R_{DS(on)}$  vs Drain Current and Gate Voltage

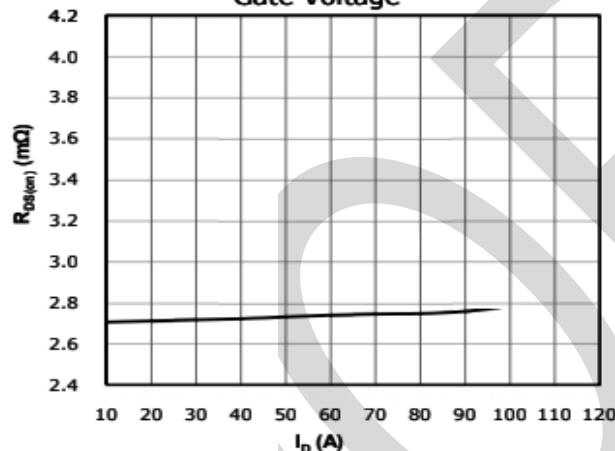


Fig 4:  $R_{DS(on)}$  vs Gate Voltage

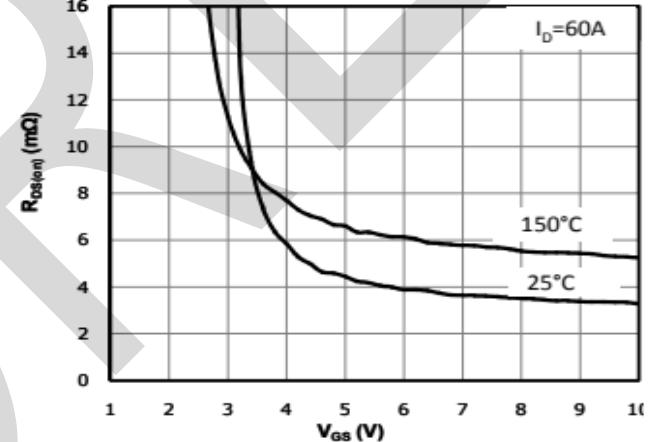


Fig 5:  $R_{DS(on)}$  vs. Temperature

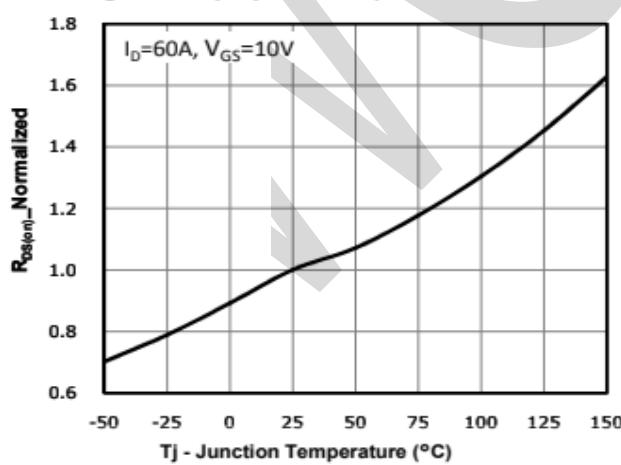
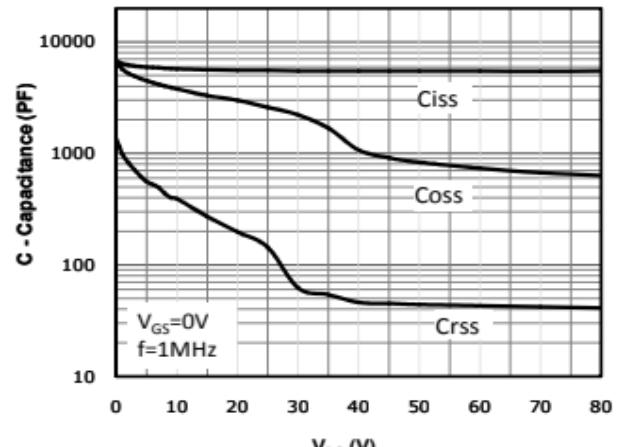
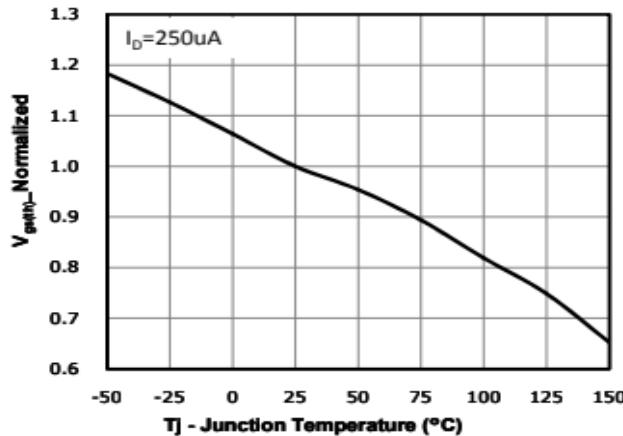


Fig 6: Capacitance Characteristics

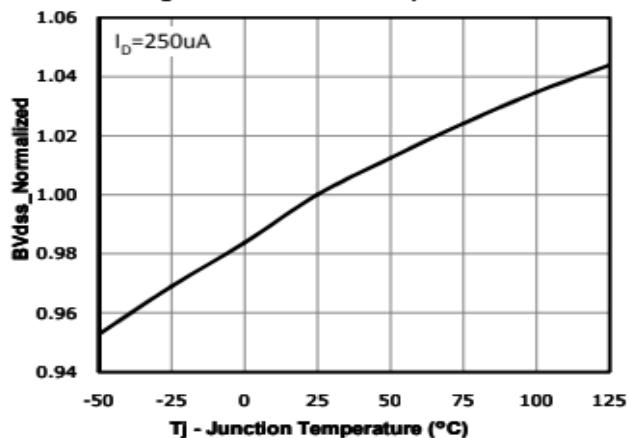


## Typical Performance Characteristics

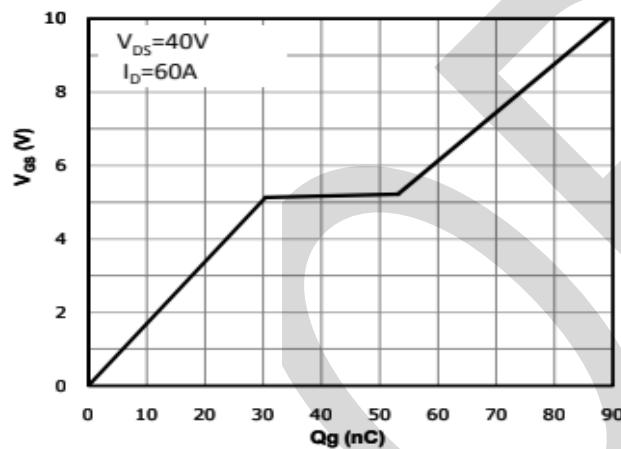
**Fig 7:  $V_{gs(th)}$  vs. Temperature**



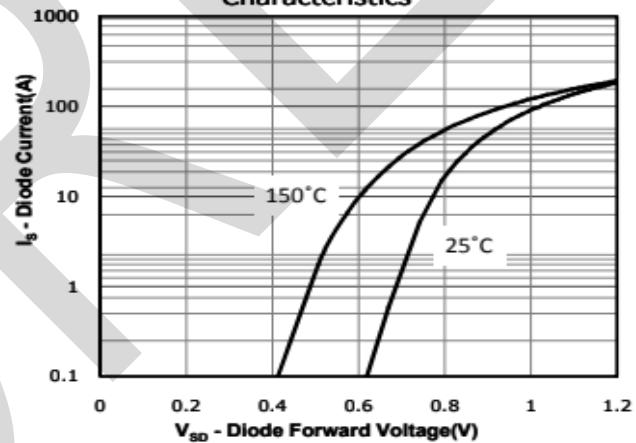
**Fig 8:  $BV_{dss}$  vs. Temperature**



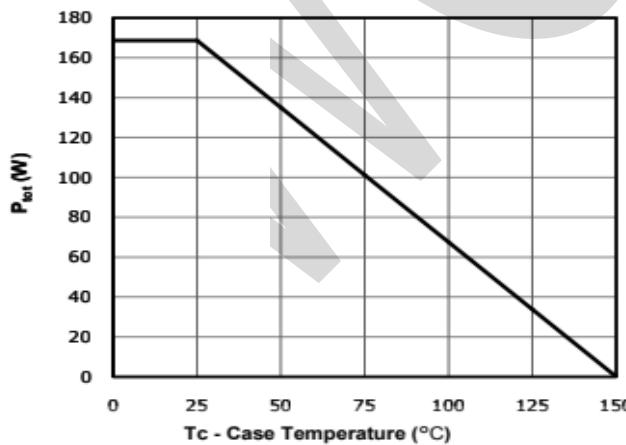
**Fig 9: Gate Charge Characteristics**



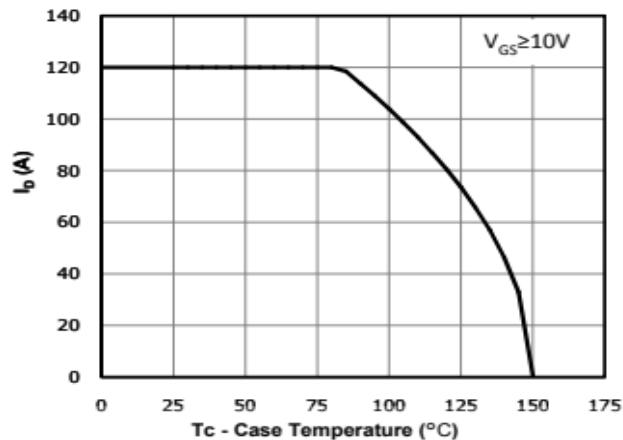
**Fig 10: Body-diode Forward Characteristics**



**Fig 11: Power Dissipation**



**Fig 12: Drain Current Derating**



## Typical Performance Characteristics

Fig 13: Safe Operating Area

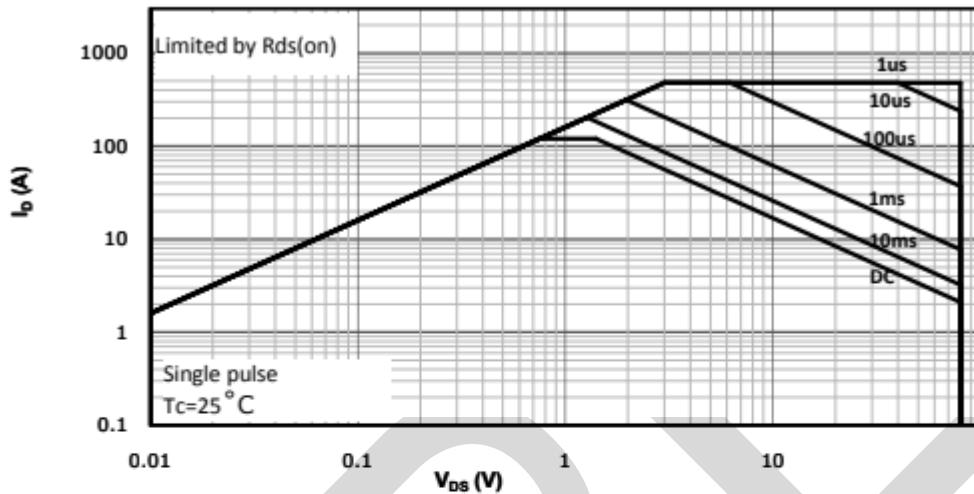
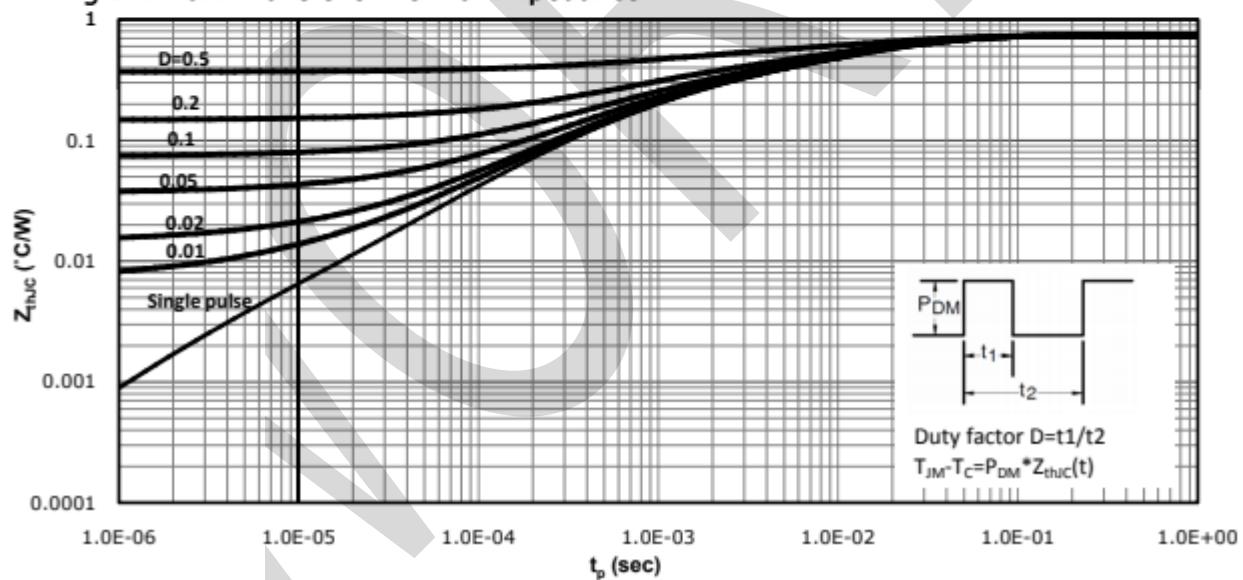


Fig 14: Max. Transient Thermal Impedance



## PACKAGE OUTLINE DIMENSIONS

**TOLL:(MM)**

