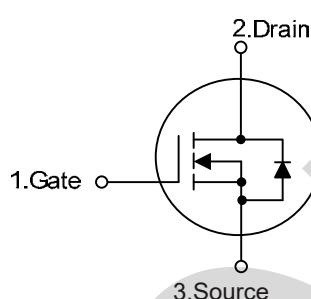
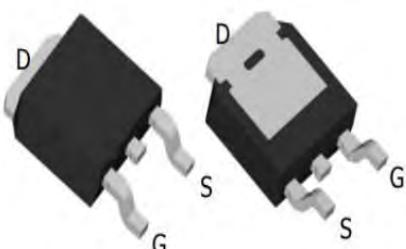


SGT N-channel Power MOSFET

MTR9R2N10D

TO-252



V_{DS}	100	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	7.2	$\text{m}\Omega$
I_D	62	A

Features

- 1、Low on – resistance
- 2、Package TO-252
- 3、SGT Power MOSFET

Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

Maximum ratings, at TA =25°C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	100	V
V_{GS}	Gate-Source voltage	± 20	V
I_S	Diode continuous forward current	62	A
I_D	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$	A
		$T_C=70^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	A
EAS	Avalanche energy, single pulsed ②	211	mJ
P_D	Maximum power dissipation	$T_C=25^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	2.0	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
--------	-----------	-----------	------	------	------	------

Static Electrical Characteristics @T_j=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.6	2.5	V
R _{D(on)}	Drain-Source On-State Resistance ③	V _{GS} =10V, I _D =15A	--	7.2	9.2	mΩ
		V _{GS} =4.5V, I _D =8A	--	10.8	14	mΩ

Dynamic Electrical Characteristics@T_j = 25°C (unless otherwise stated)

C _{iss}	Input Capacitance	V _{DS} =50V, V _{GS} =0V , f=1MHz	--	2330	--	pF
C _{oss}	Output Capacitance		--	465	--	pF
C _{rss}	Reverse Transfer Capacitance		--	21	--	pF
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V , f=1MHz	--	3	--	Ω
Q _g (10V)	Total Gate Charge	V _{DS} =50V, I _D =20A , V _{GS} =10V	--	39.7	--	nC
Q _{gs}	Gate-Source Charge		--	6.8	--	nC
Q _{gd}	Gate-Drain Charge		--	9.2	--	nC

Switching Characteristics

Td(on)	Turn-on Delay Time	$V_{DD}=50V$, $V_{GS}=10V$, $I_D=10A$, $R_G=6\Omega$	--	14.6	--	ns
Tr	Turn-on Rise Time		--	8	--	ns
Td(off)	Turn-Off Delay Time		--	22.2	--	ns
Tf	Turn-Off Fall Time		--	6.4	--	ns

Source -Drain Diode Characteristics @Tj = 25°C (unless otherwise stated)

VSD	Forward on voltage ③	$I_{SD}=10A, V_{GS}=0V$	--	0.78	1.2	V
Trr	Reverse Recovery Time	$I_F=20A$,	--	45	--	ns
Qrr	Reverse Recovery Charge	$dI/dt=100A/\mu s$	--	185	--	nC

Notes: ① Pulse width limited by maximum allowable junction temperature

② Limited by TJmax, starting TJ = 25°C, L = 0.1mH, RG = 25Ω, IAS = 65A, VGS = 10V. Part not recommended for use above this value

③ Pulse width ≤ 300μs; duty cycle≤ 2%.

Typical Characteristics

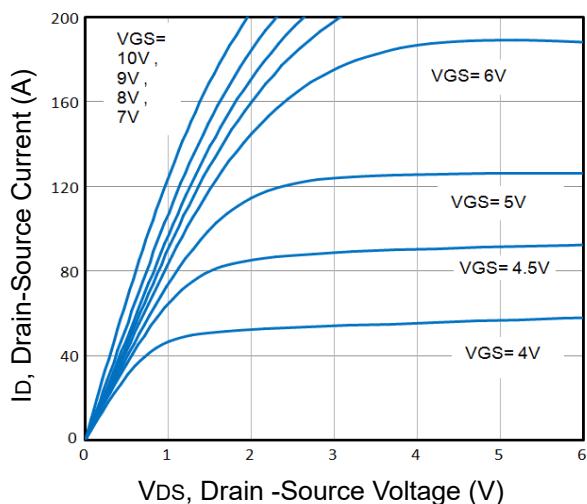


Fig1. Typical Output Characteristics

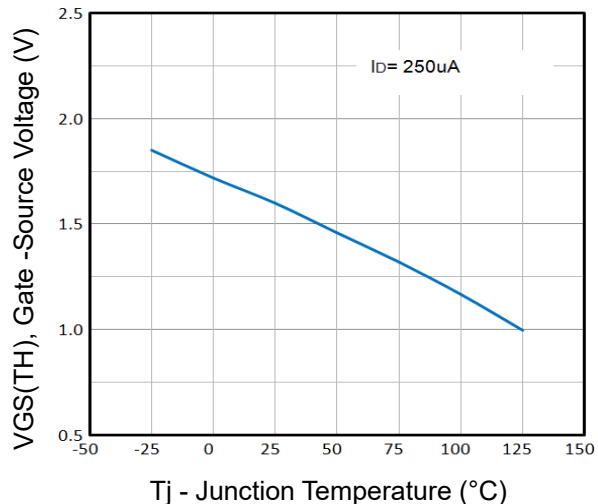


Fig2. $V_{GS(TH)}$ Voltage Vs. Temperature

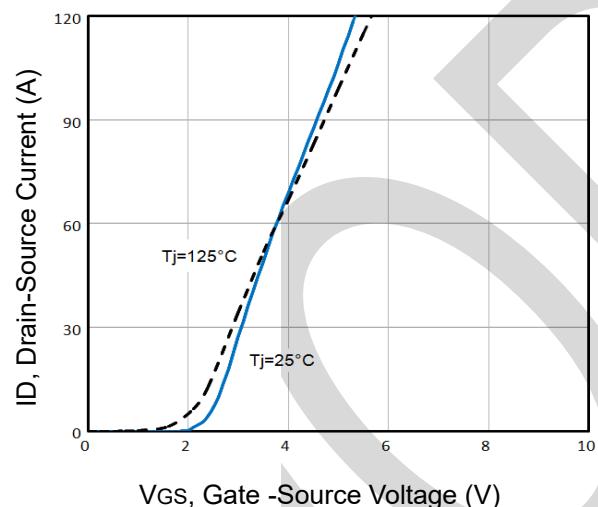


Fig3. Typical Transfer Characteristics

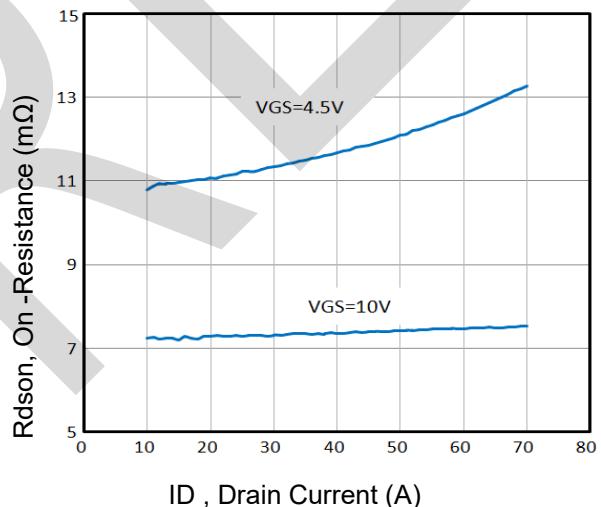


Fig4. On-Resistance vs. Drain Current and Gate Voltage

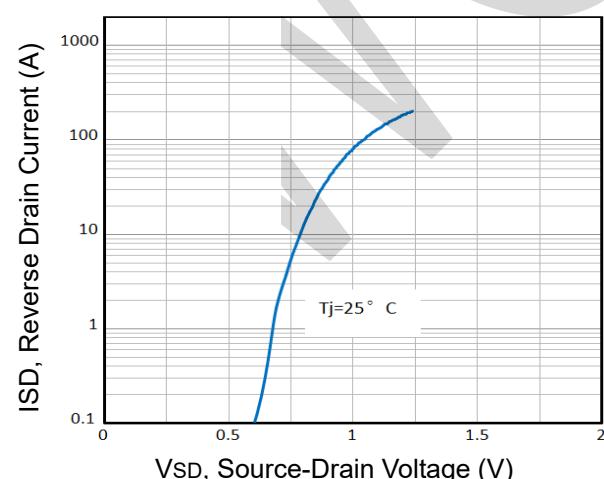


Fig5. Typical Source-Drain Diode Forward Voltage

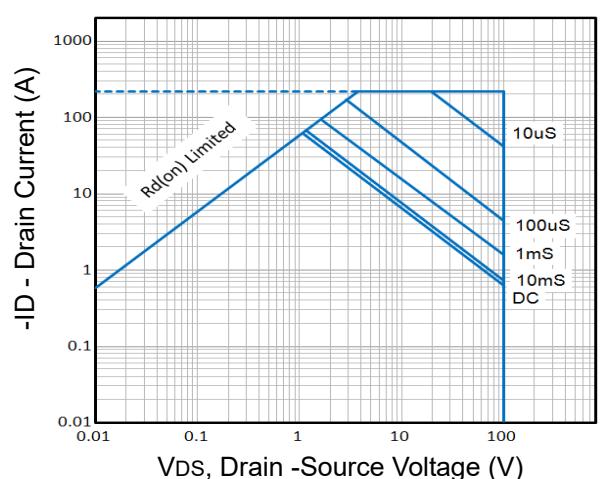


Fig6. Maximum Safe Operating Area

Typical Characteristics

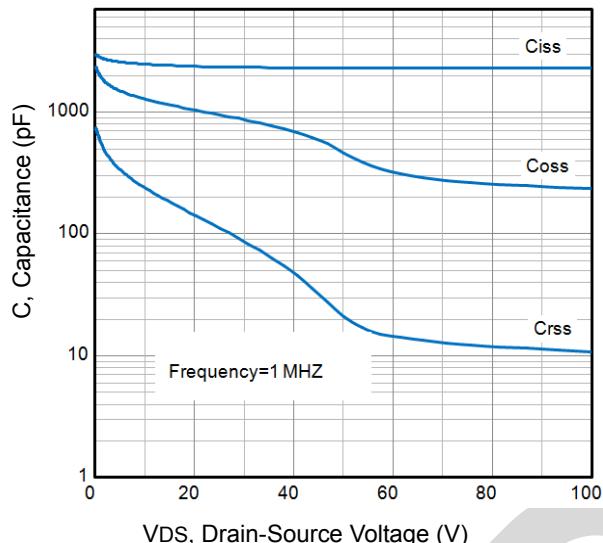


Fig7. Typical Capacitance Vs. Drain-Source Voltage

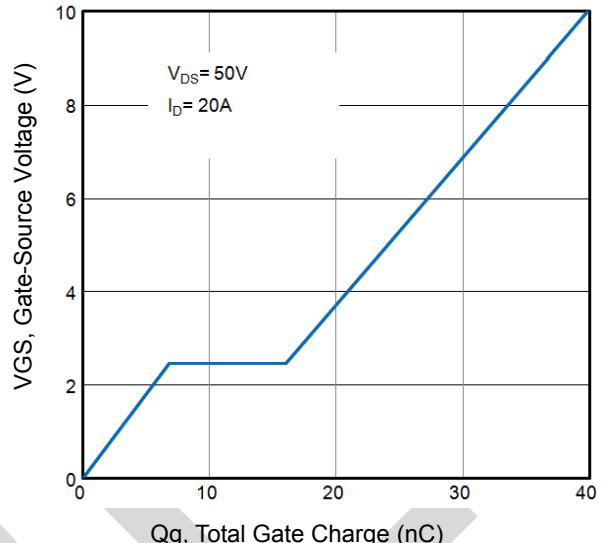


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

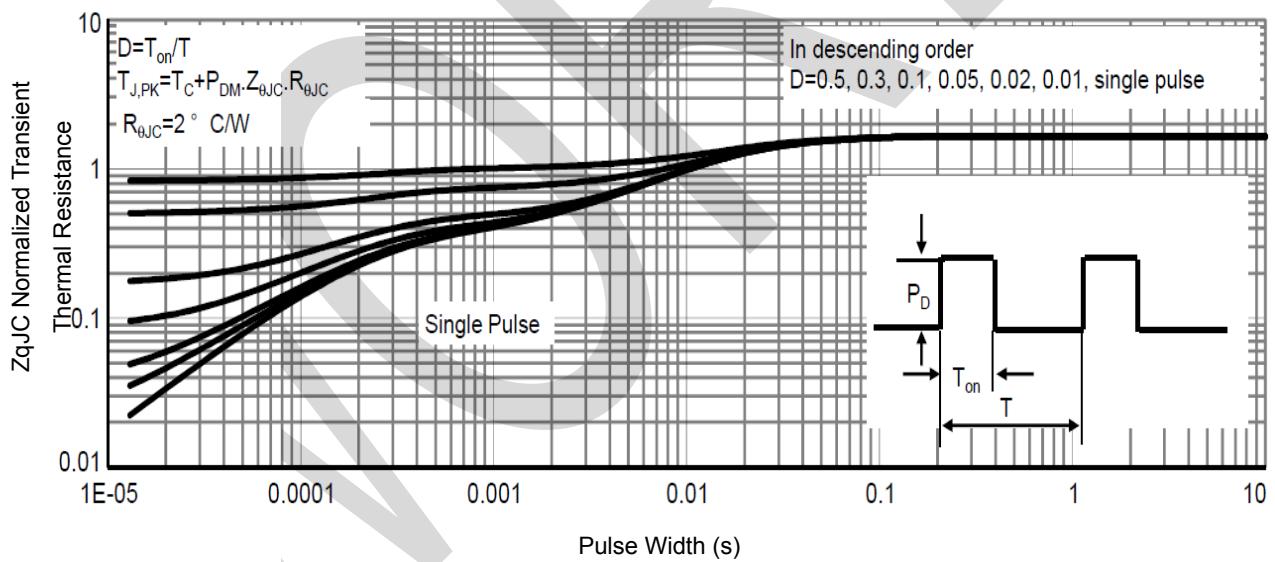
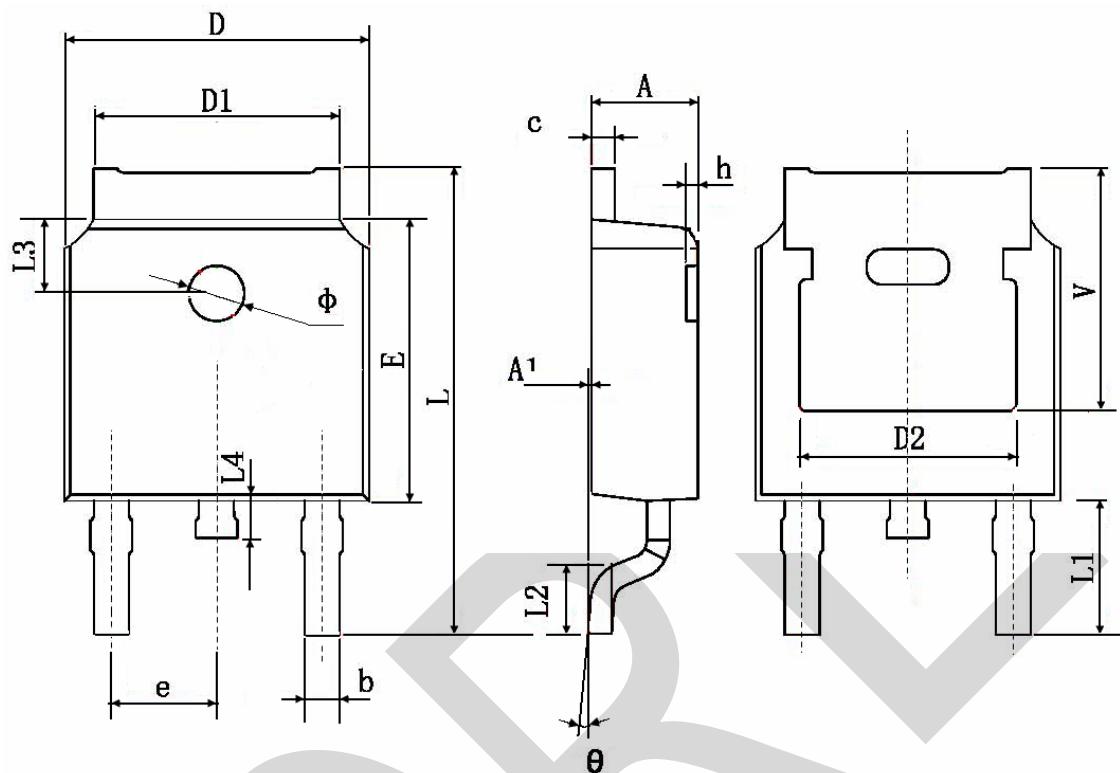


Fig9. Normalized Maximum Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	