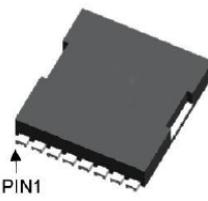


SGT N-channel Power MOSFET

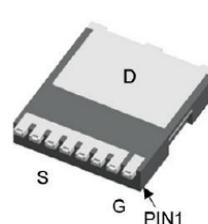
TOLL
MTR3R7N10TL

TOLL Top View

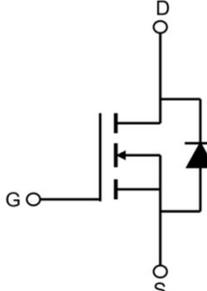


PIN1

TOLL Bottom View



D
S
G
PIN1



V_{DS}	100	V
$R_{DS(on),TYP@ V_{GS}=10 V}$	3.1	$m\Omega$
I_D	200	A

Features

- 1、 Low on – resistance
- 2、 Package TOLL
- 3、 SGT N-channel Power MOSFET

Applications

- 1、 Load Switch for Portable Devices
- 2、 DC/DC Converter

Maximum ratings, at $T_A = 25^\circ C$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V_{(BR)DSS}$	Drain-Source breakdown voltage	100	V	
V_{GS}	Gate-Source voltage	± 20	V	
I_D	Continuous drain current @ $V_{GS}=10V$	$T_C = 25^\circ C$ (Silicon limit)	220	A
		$T_C = 25^\circ C$ (Package limit)	200	A
		$T_C = 100^\circ C$ (Silicon limit)	110	A
I_{DM}	Pulse drain current tested ①	$T_C = 25^\circ C$	480	A
E_{AS}	Avalanche energy, single pulsed ②	1156	mJ	
P_D	Maximum power dissipation	$T_C = 25^\circ C$	340	W
T_{STG}, T_J	Storage and Junction Temperature Range	-55 to 150	$^\circ C$	

Thermal Characteristics

Symbol	Parameter	Typical	Unit
R θ JC	Thermal Resistance, Junction-to-Case	0.55	°C/W
R θ JA	Thermal Resistance, Junction-to-Ambient	62	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
--------	-----------	-----------	------	------	------	------

Static Electrical Characteristics @ T_j=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2.0	3.0	4.0	V
R _{DS(on)}	Drain-Source On-State Resistance ④	V _{GS} =10V, I _D =50A	--	3.1	3.7	mΩ

Dynamic Electrical Characteristics @ T_j = 25°C (unless otherwise stated)

C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V , f=1MHz	--	9035	--	pF
C _{oss}	Output Capacitance		--	826	--	pF
C _{rss}	Reverse Transfer Capacitance		--	578	--	pF
R _g	Gate Resistance	V _{GS} =0V, f=1MHz V _{DS} =0V	--	1.4	--	Ω
Q _g (10V)	Total Gate Charge	V _{DS} =50V, I _D =50A , V _{GS} =10V, I _D =20A	--	90	--	nC
Q _{gs}	Gate-Source Charge		--	30	--	nC
Q _{gd}	Gate-Drain Charge		--	20	--	nC

Switching Characteristics

Td(on)	Turn-on Delay Time	V _{DS} =50V, V _{GS} =10V, R _L =3.0Ω, T _J =25°C	--	27	--	ns
Tr	Turn-on Rise Time		--	32	--	ns
Td(off)	Turn-Off Delay Time		--	50	--	ns
Tf	Turn-Off Fall Time		--	27	--	ns

Source- Drain Diode Characteristics@ T_J = 25°C (unless otherwise stated)

VSD	Forward on voltage	I _{SD} =50A, V _{GS} =0V	--	0.82	1.2	V
Trr	Reverse Recovery Time	I _F =30A, di/dt=500A/μs	--	45	--	ns
Qrr	Reverse Recovery Charge	I _F =30A, di/dt=500A/μs	--	80	--	nC

- NOTE: ① Repetitive rating; pulse width limited by max junction temperature.
 ② Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH, R_G = 25Ω, I_{AS} = 9A, V_{GS} = 10V. Part not recommended for use above this value
 ③ The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C.
 ④ Pulse width ≤ 380μs; duty cycle ≤ 2%.

Typical Characteristics

Fig 1: Output Characteristics

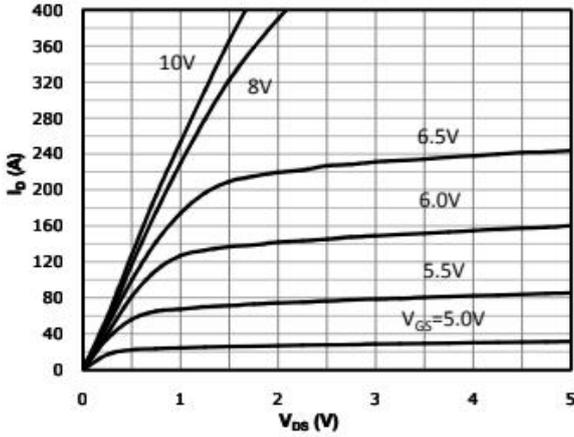


Fig 2: Transfer Characteristics

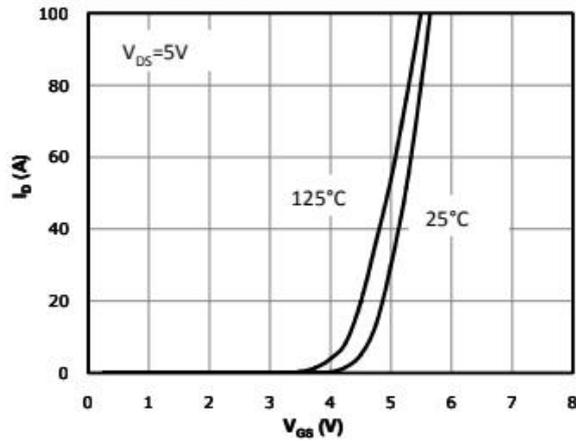


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

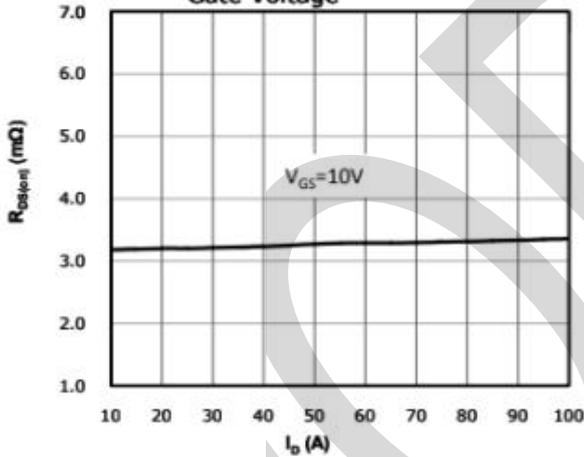


Fig 4: $R_{DS(on)}$ vs Gate Voltage

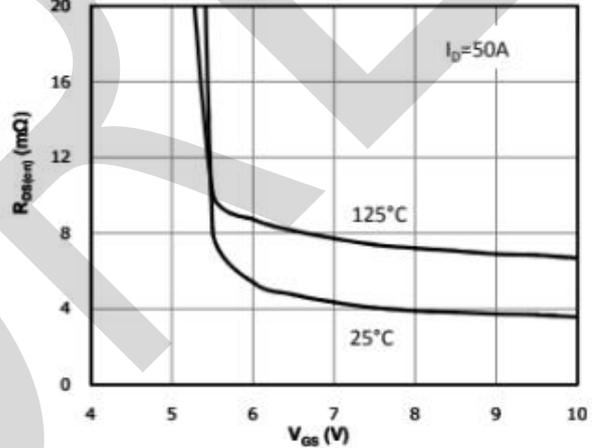


Fig 5: $R_{DS(on)}$ vs. Temperature

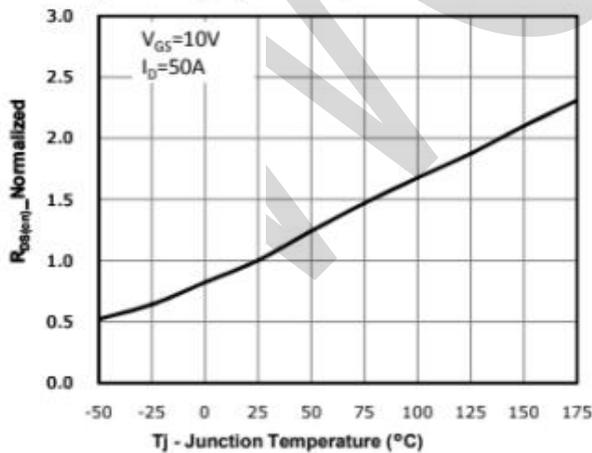
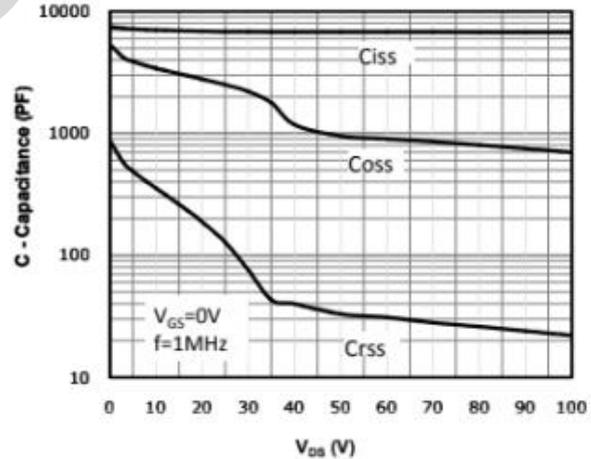


Fig 6: Capacitance Characteristics



Typical Characteristics

Fig 7: Gate Charge Characteristics

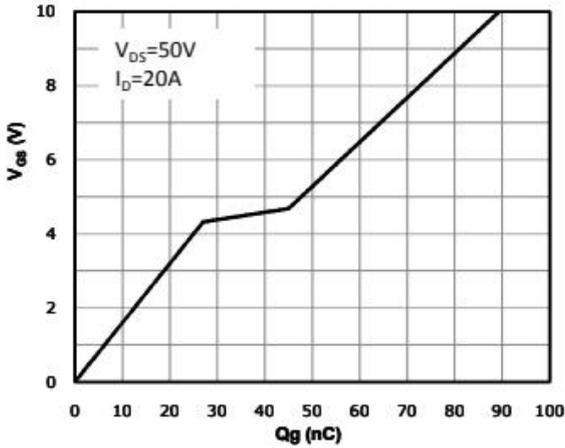


Fig 8: Body-diode Forward Characteristics

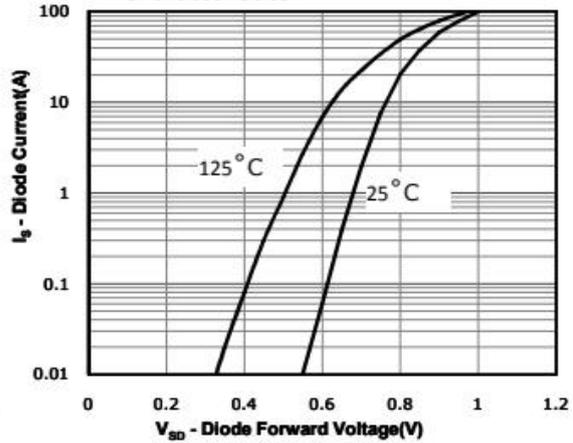


Fig 9: Power Dissipation

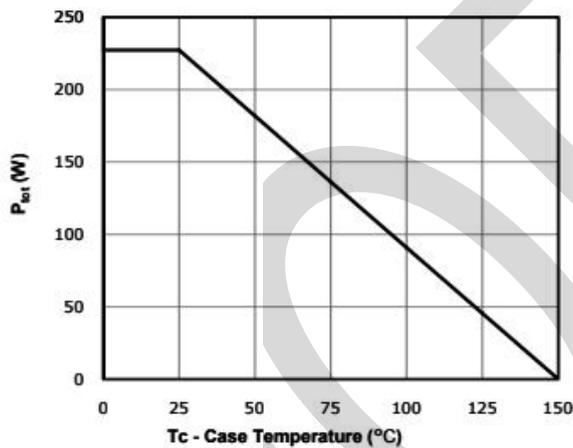


Fig 10: Drain Current Derating

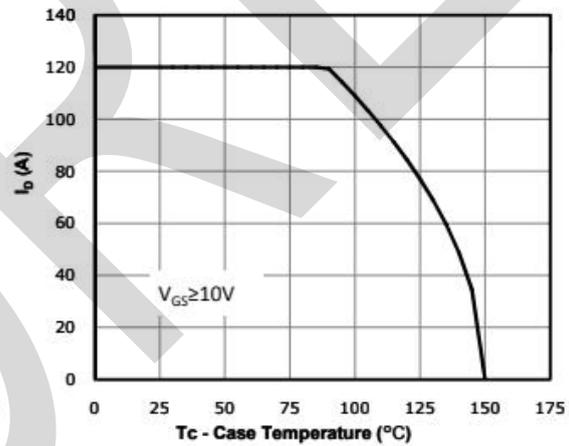
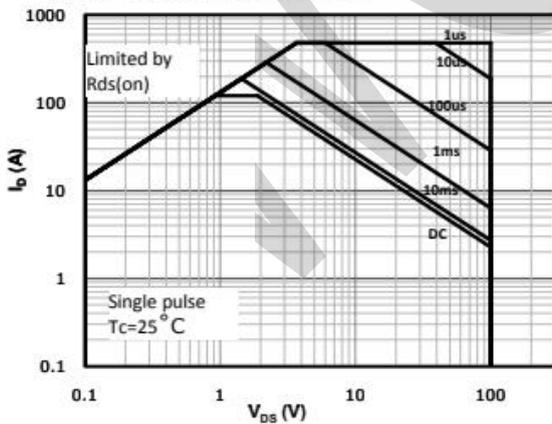
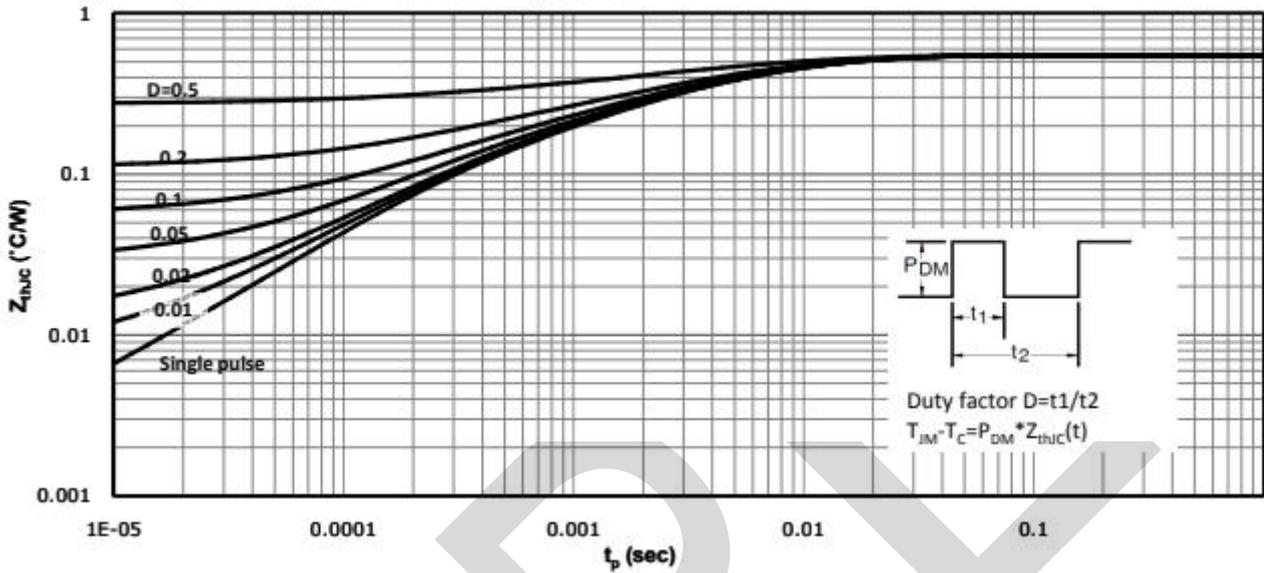


Fig 11: Safe Operating Area



Typical Characteristics

Fig 12: Max. Transient Thermal Impedance



PACKAGE OUTLINE DIMENSIONS

TOLL:(MM)

