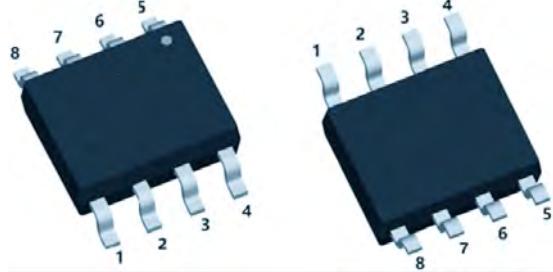


## Trench N-channel Power MOSFET

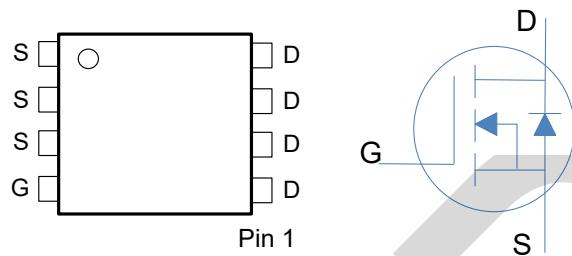
**MSR009N03P8 SOP8**



$V_{DS}$	30	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	7.5	mΩ
$I_D$	12	A

### Features

- 1、Low on – resistance
- 2、High power package (SOP8)
- 3、TrenchFET Power MOSFET
- 4、Halogen free



### Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

**Maximum ratings, at  $T_A = 25^\circ\text{C}$ , unless otherwise specified**

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	30	V
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_c=25^\circ\text{C}$	A
		$T_c=100^\circ\text{C}$	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^\circ\text{C}$	A
$EAS$	Avalanche energy, single pulsed ②	144	mJ
$P_D$	Maximum power dissipation	$T_c=25^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	20	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	36	°C/W

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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### Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	34	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.5	V
R <sub>D(on)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =12A	--	7.5	9.0	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	--	11	15	mΩ
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =10A	--	15	--	S

### Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	--	1600	--	pF
C <sub>oss</sub>	Output Capacitance		--	280	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	195	--	pF
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz	--	2.3	--	Ω
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>DS</sub> =25V, I <sub>D</sub> =12A , V <sub>GS</sub> =10V	--	35	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	6	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	11	--	nC

## Switching Characteristics

Td(on)	Turn-on Delay Time	V <sub>DS</sub> =15V, R <sub>L</sub> =0.75Ω, R <sub>G</sub> =3Ω, V <sub>GS</sub> =10V	--	9	--	ns
Tr	Turn-on Rise Time		--	26	--	ns
Td(off)	Turn-Off Delay Time		--	35	--	ns
Tf	Turn-Off Fall Time		--	8	--	ns

Notes 1.The maximum current rating is package limited.

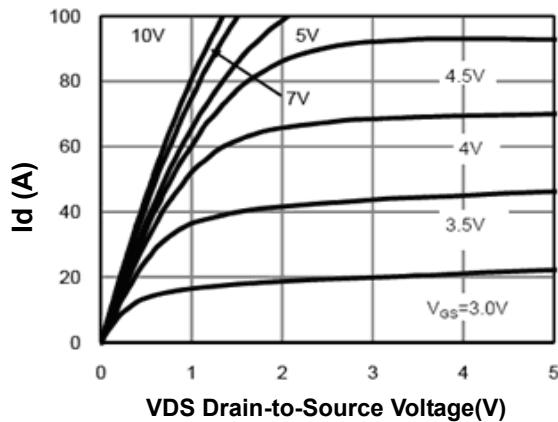
Notes 2.Repetitive Rating: Pulse width limited by maximum junction

Temperature Notes 3.EAS condition: T<sub>J</sub>=25°C,V<sub>DD</sub>=15V,V<sub>G</sub>=10V, R<sub>G</sub>=25Ω,

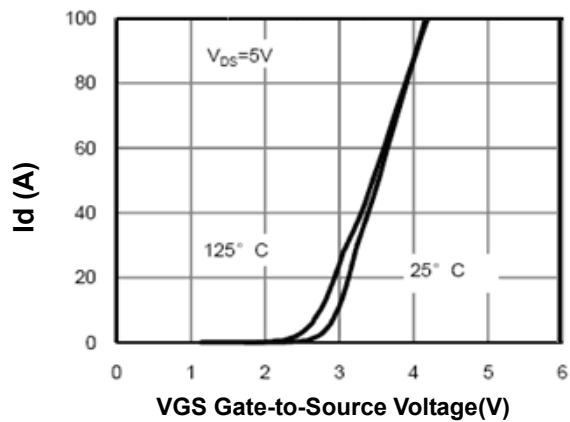
ORV

## Typical Characteristics

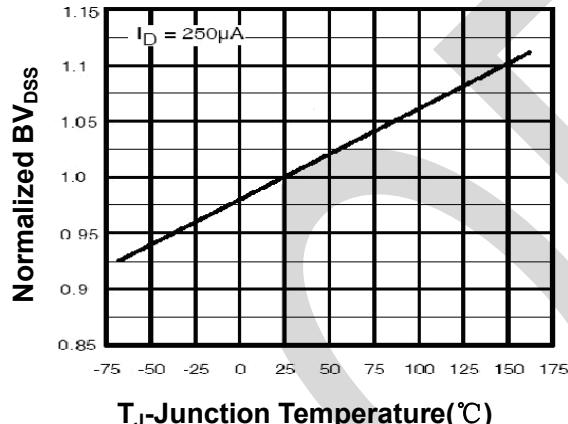
**Figure 1. Output Characteristics**



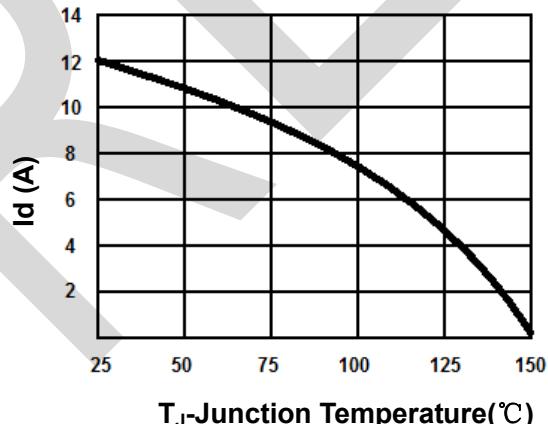
**Figure 2. Transfer Characteristics**



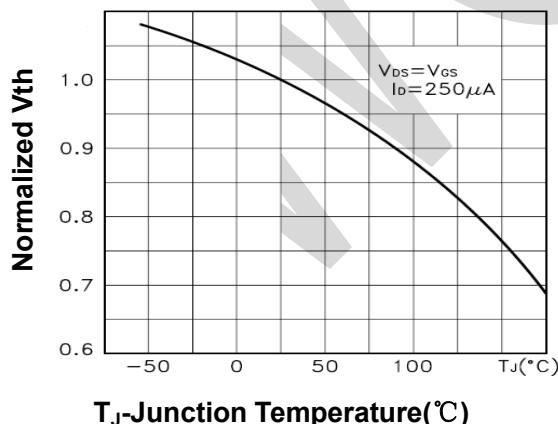
**Figure 3. Max  $BV_{DSS}$  vs Junction Temperature**



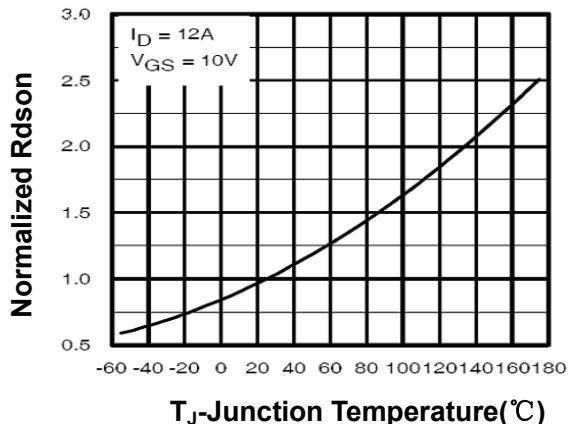
**Figure 4. Drain Current**



**Figure 5.  $V_{GS(th)}$  vs Junction Temperature**

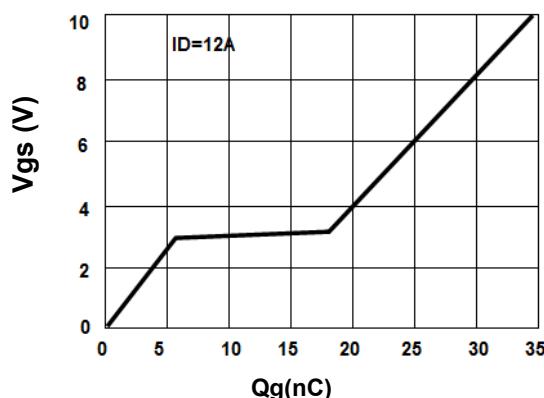


**Figure 6.  $R_{DS(ON)}$  vs Junction Temperature**

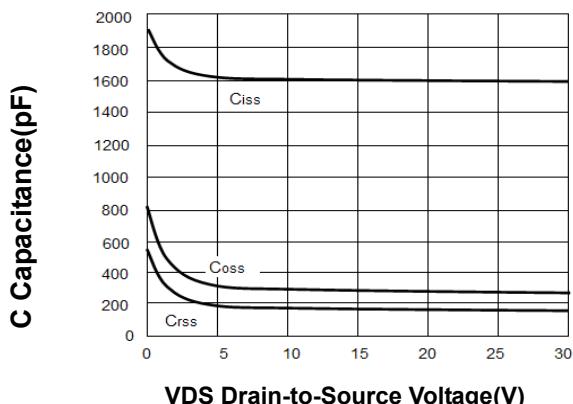


## Typical Characteristics

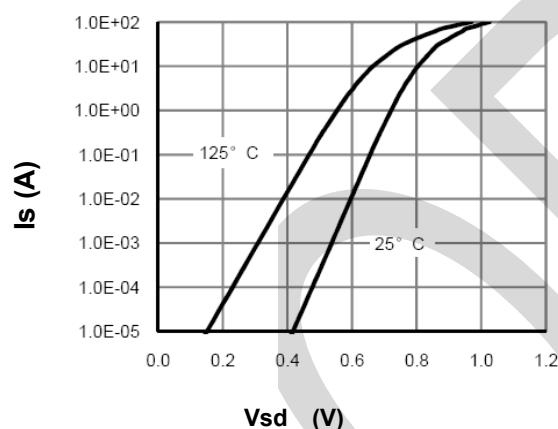
**Figure 7. Gate Charge Waveforms**



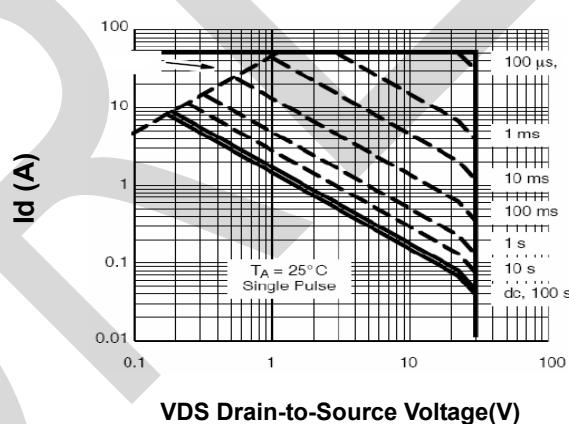
**Figure 8. Capacitance**



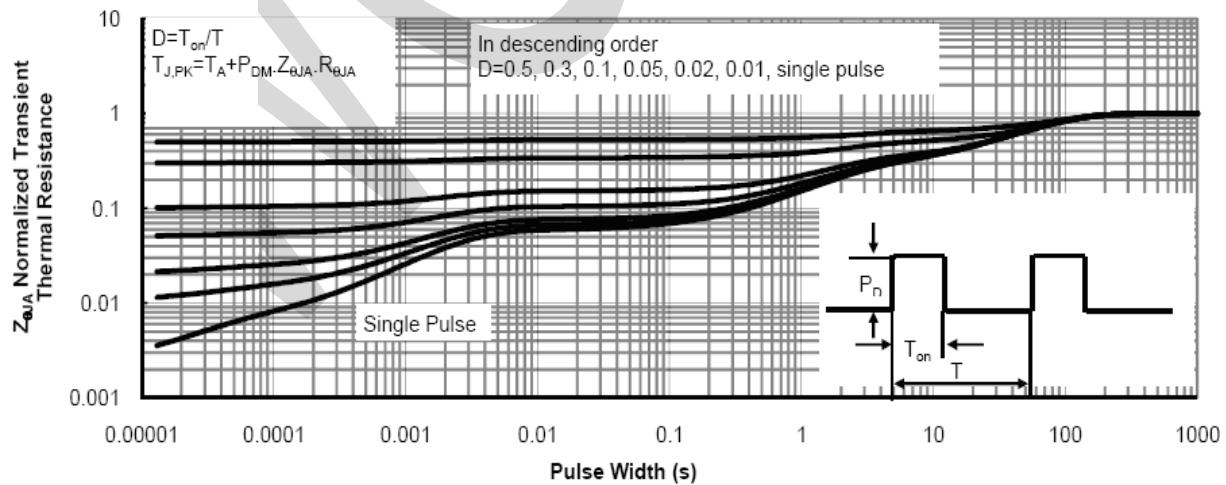
**Figure 9. Body-Diode Characteristics**



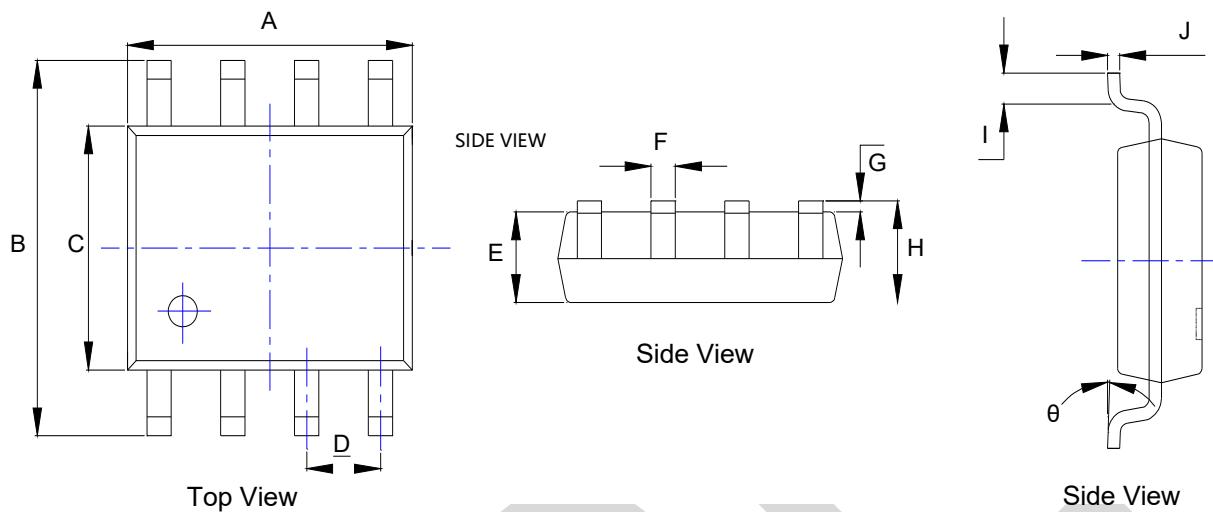
**Figure 10. Maximum Safe Operating Area**



**Figure 11. Normalized Maximum Transient Thermal Impedance**



## PACKAGE OUTLINE DIMENSIONS



## SOP8 mechanical data

UNIT		A	B	C	D	E	F	G	H	I	J	$\theta$
mm	min	4.80	5.80	3.80	1.27 TYP	1.35	0.33	0.10	1.35	0.40	0.17	0°
	max	5.00	6.20	4.00		1.55	0.51	0.25	1.75	1.27	0.25	8°
mil	min	157.4	228.3	149.6	50.0 TYP	53.1	13.0	3.9	53.1	15.7	6.7	0°
	max	196.9	244.1	157.5		61.1	20.1	9.9	68.9	50.0	9.8	8°

## SOP8 Suggested Pad Layout

