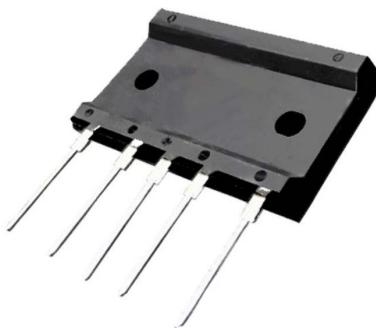


Low VF Three-phase Bridge Rectifiers

DXT6008L



Features

- Glass Passivated Chip Junction
- Low IRRM
- Low VF
- High VRMM
- Special frame design for heat dissipation

Benefits

- Case: DXT
- Terminals: Solderable Per MIL-STD-750
- Reduced power loss and switching transistor

Parameter	Symbols	DXT6008L	Units
Maximum Repetitive Peak Reverse Voltage	VRRM	800	V
Maximum RMS voltage	VRMS	560	V
Maximum DC Blocking Voltage	VDC	800	V
Average Rectified Output Current	Io	60	A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	IFSM	450	A
I ² t rating for fusing (1ms < t < 10ms)	I ² t	840	A ² s
Type Forward Voltage at 30.0A	VF	0.93	V
Maximum Forward Voltage at 30.0 A		1.0	
Maximum DC Reverse Current @TA=25 °C at Rated DC Blocking Voltage @TA=125 °C	IR	10 500	μA
Junction to ambient, without heatsink ¹	R _{θJA}	16	°C/W
Operating and Storage Temperature Range	T _j , T _{stg}	-55 ~ +150	°C
Note: 1. Mounted on glass epoxy PC board with 4×1.5"×1.5" (3.81×3.81 cm) copper pad.			

RATINGS AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)

Fig.1 Current Derating, Case

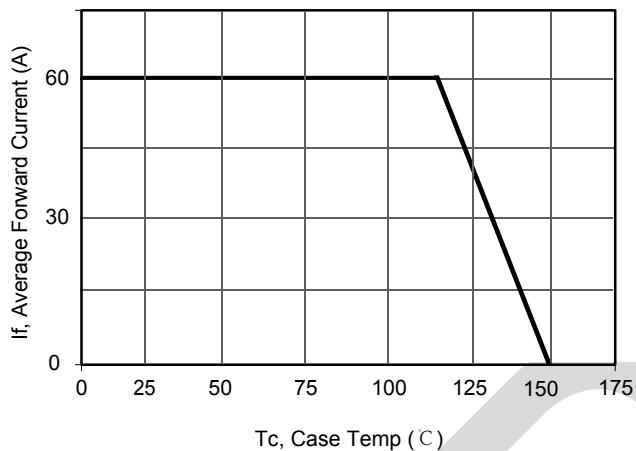


Fig.2 Maximum Non-Repetitive Peak Forward Surge Current

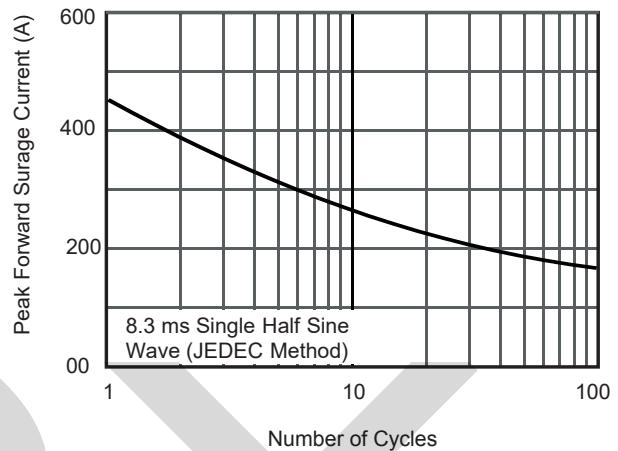


Fig.3 Typical Forward Voltage

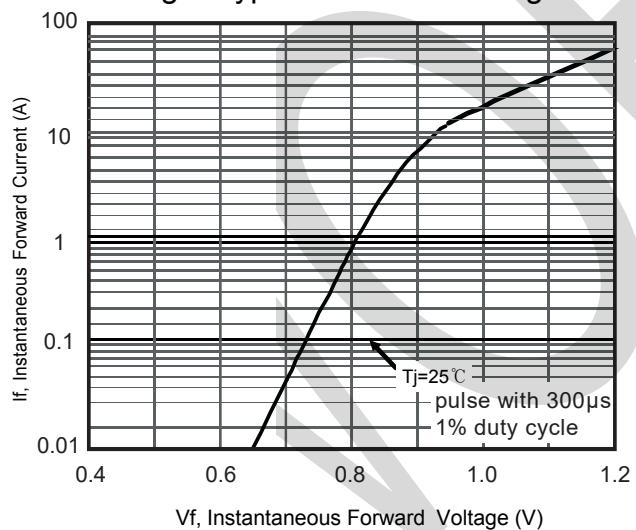
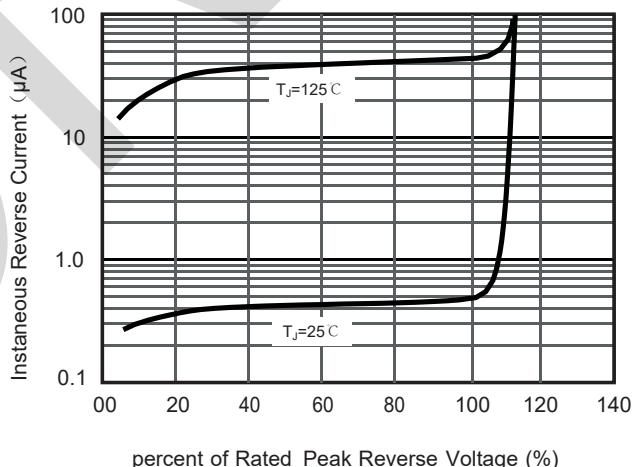
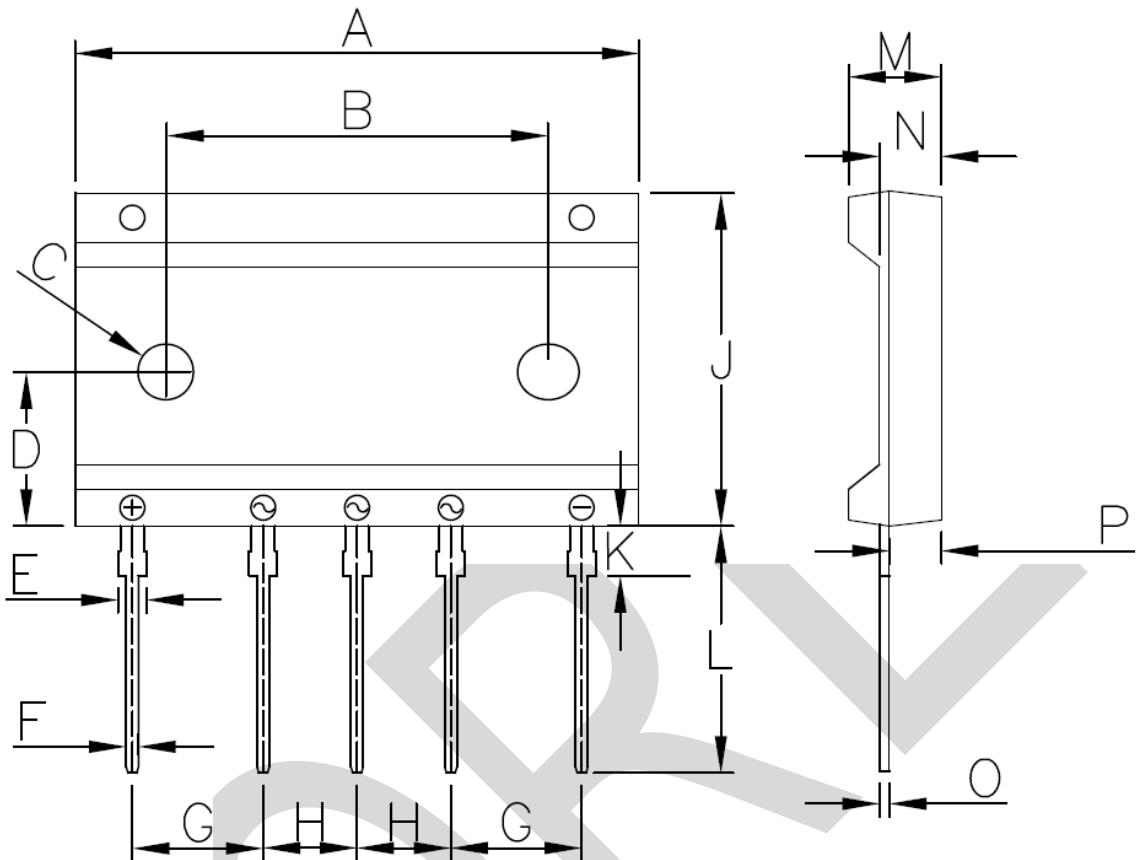


Fig.4 Typical Reverse Characteristics



PACKAGE OUTLINE DIMENSIONS



DXT mechanical data: unit mm(inch)

	A	B	C	D	E	F	G	H	J	K	L	M	N	O	P
max	46.1	31.1	4.8	12.8	2.5	1.3	10.9	8.0	27.5	4.2	20.5	8.4	5.9	1.0	4.8
min	45.3	30.9	4.4	12.2	2.1	0.7	10.3	7.2	26.5	3.8	19.5	7.6	5.1	0.6	4.2
max	1.81	1.22	0.19	0.51	1.3	0.10	0.43	0.32	1.09	0.17	0.81	0.33	0.24	0.04	0.19
min	1.78	1.21	0.17	0.48	1.0	0.02	0.40	0.28	1.04	0.15	0.76	0.29	0.20	0.5	0.16

