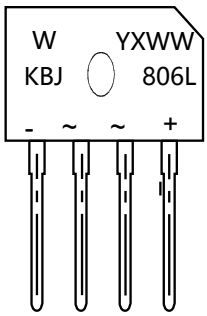
World Internation (China) Limited

KBJ806L

18-NOV-2019

Low VF Bridge Rectifiers



Features

- Glass Passivated Chip Junction
- Low I_{RRM}
- Low V_F
- High V_{RRM}

Benefits

- Case: KBJ
- Terminals: Solderable Per MIL-STD-750
- Reduced power loss and switching transistor
- Reduced snubbing

PINNING

PIN	DESCRIPTION
1	Input Pin（~）
2	Input Pin（~）
3	Output Anode（+）
4	Output Cathode（-）

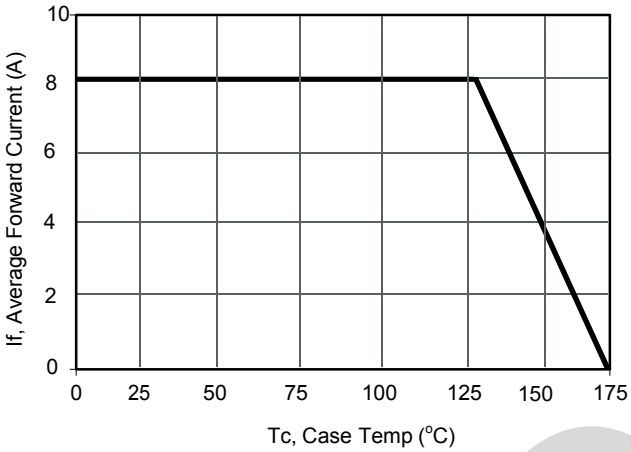
Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.
Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

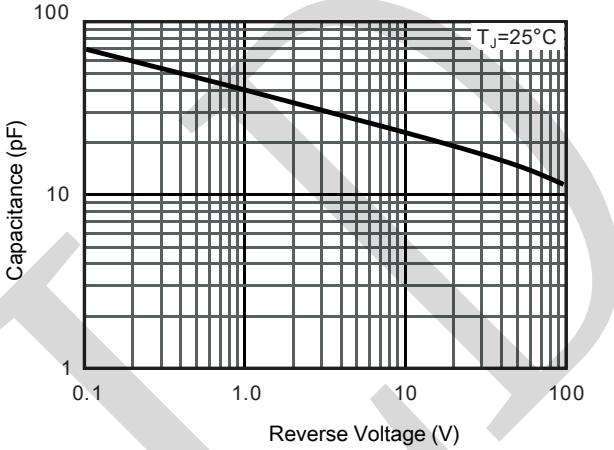
Parameter	Symbols	KBJ806L	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	600	V
Maximum RMS voltage	V_{RMS}	420	V
Maximum DC Blocking Voltage	V_{DC}	600	V
Average Rectified Output Current	I_o	8.0	A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I_{FSM}	120	A
Maximum Forward Voltage at 4.0 A	V_F	0.90	V
Maximum DC Reverse Current @TA=25 °C at Rated DC Blocking Voltage @TA=125 °C	I_R	10 500	μA
Typical Junction Capacitance（Note1）	C_j	25	pF
Operating and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4 VDC.
2.Mounted on glass epoxy PC board with 4×1.5 æ1.5 （3.81×3.81 cm）copper pad

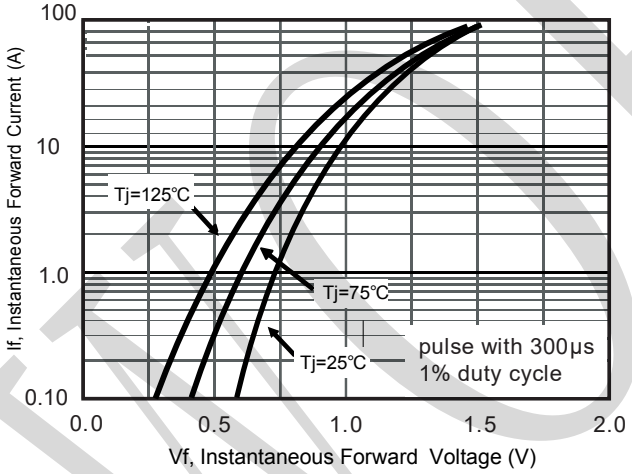
RATINGS AND CHARACTERISTICS CURVES (TA = 25 ˚C unless otherwise noted)



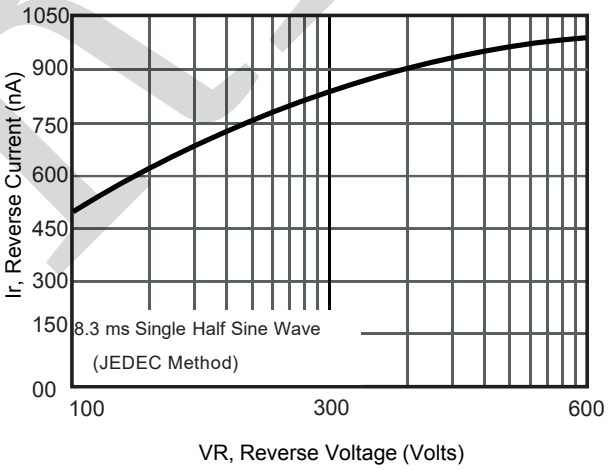
Current Derating, Case



Typical Junction Capacitance



Typical Forward Voltage



Typical Reverse Current

PACKAGE OUTLINE DIMENSIONS

Note:unit In(mm)